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**SoftJin Announces Building Block for Logic Optimization and Mapping;
Synthesis Engine Reduces Time-to-Deployment for Programmable Platform Design Tools**

SANTA CLARA, Calif. & **BANGALORE**, India—July 18, 2006--SoftJin, an EDA (Electronic Design Automation) software development services company, today announced its new **Programmable Synthesis Engine** (PSE), a logic optimization and mapping software product that is customizable for a variety of programmable platform architectures, such as Look-up-Table (LUT) or Multiplexer (MUX) based configurable logic architectures.

SoftJin's PSE is a customizable engine that allows its users to derive the most from the underlying architecture of a programmable platform. It delivers better Quality of Results (QoR) when compared to traditional FPGA and ASIC synthesis tools. SoftJin customizes each PSE to specific programmable platform architecture and for integration with placement and routing tools.

PSE will be demonstrated at the Design Automation Conference in San Francisco, July 24-27 at SoftJin's booth # 1409.

"Programmable Platform suppliers of FPGA, structured ASIC and other innovative programmable fabrics need to provide design tools to their users that are customized and optimized for their specific architectures for the best results," said Nachiket Urdhwareshe, SoftJin's CEO. "PSE allows these suppliers to provide a customized logic synthesis solution with reduced time-to-deployment and with better results."

Who Uses PSE

PSE is licensed by programmable platform vendors for use by their customers who are FPGA, ASIC or SoC designers. PSE can also be licensed by system-level EDA companies who would like to extend their offerings into the RTL domain.

Why Use PSE

Before PSE, emerging programmable platform vendors developed their own customized synthesis solution from scratch, which is costly, risky and highly time consuming, or licensed a standard synthesis solution, targeted to mainstream architectures, which then had to be customized to their specific offerings. This latter path involves hefty licensing and customization fees. SoftJin's PSE is the first offering in the market that provides a viable quality plus cost combination to these emerging vendors for their synthesis solution.

Also, before PSE, system-level EDA tool vendors curtailed their output to RTL output and relied on standard synthesis vendors to complete the rest of the design flow. While this RTL handoff enabled vendor neutrality, it also caused end users to buy expensive synthesis tool licenses and led to efficiency loss in the handoff. While keeping the vendor neutrality in play, SoftJin's PSE shifts this handoff further down the design flow possibly to a placed-netlist level. By shifting the handoff further down the design flow, there is no loss in efficiency at the RTL handoff and turn-around time is minimized by an integrated solution.

More about PSE

SoftJin's PSE is a logic synthesis engine that is customizable for a variety of Programmable Platforms including LUT and MUX based architectures. PSE accepts RTL Verilog and VHDL, performs a host of technology independent and dependent optimizations followed by architecture specific mapping to generate a delay and area optimal design implementation ideally suited to the targeted programmable platform.

PSE offers the following key benefits:

a) PSE's technology delivers value by offering highly optimized logic synthesis solutions for custom architectural elements, while continuing to deliver comparable benchmark performance for current off-the-shelf FPGAs. It delivers this by integrating a modular plug-n-play approach of technology components such as Module Generators and Technology Mappers, in conjunction with standard off-the-shelf parsers and elaborators. Easy customizability of PSE's Module Generators and Technology Mappers enables SoftJin to offer a solution that takes full advantage of data-path elements, custom IP blocks and random logic available in target architectures. This

approach enables a custom logic synthesis tool with improved QoR in comparison to off-the-shelf generic synthesis tools built for traditional FPGA and ASIC architectures.

b) In the changing face of tool pricing, SoftJin's approach allows the Programmable Platform supplier to consider a value proposition that is both higher in QoR for their unique architecture and lower in development costs. This means their customers will have a highly sophisticated performance tuned synthesis tool that is attractively priced for cost sensitive designers.

c) For second generation behavioral synthesis tool vendors, that are targeting programmable platform vendors, rather than stopping at RTL output, integrating PSE into their solution offers a platform neutral approach all the way to netlist without trading on performance or high licensing costs. With possible integrations with programmable platform vendor specific physical design tools or a library based approach that abstracts the data-path oriented elements all the way up the tool chain, it is possible to deliver uniquely integrated solutions.

Features

a) PSE's Technology Mapper implements the optimized abstract logic and the abstract sequential elements present in the input onto target technology in an efficient manner such that the resources on the target device are effectively used in conjunction with minimum delay. Within the overall constraint of optimizing the solution for minimum delay by driving the Mapper in a timing-driven mode, it optimizes the design for minimum area. With the customization that is possible for the Technology Mapper, this solution can be applied to LUT-based, MUX-based or even logic gate-based architectures.

b) PSE's Module Generator recognizes and maps a variety and growing list of higher level primitives such as arithmetic operators to the available data-path elements of the architecture, such as add, sub, add sub, mul, div, compare and barrel shifter, thus achieving a highly efficient implementation.

c) Along with PSE, SoftJin offers PSE's customization and integration services, whereby SoftJin takes the responsibility of customizing PSE for programmable platform vendor's architecture and integrating PSE with downstream placement and routing tools.

Price and Availability

PSE is available for beta evaluation now and is licensed as object code to Programmable Platform vendors and ESL tool vendors to include and sell as part of their design tool flow. The annual license fee varies and depends on the underlying architecture of the Programmable Platform.

SoftJin products are sold directly in the USA, Europe and India and through channel partners in Japan and Asia Pacific.

About SoftJin

SoftJin Technologies Pvt. Ltd. develops customized EDA tools for the specific requirements of semiconductor and EDA companies using a combination of EDA software development services and Re-usable Building Blocks. SoftJin's customized EDA software development approach offers the advantages of enhanced EDA software capability, flexible capacity and cost savings to customers. SoftJin was recently (June 5, 2006) included in a list of Indian startups to watch by Red Herring magazine. More information is available at www.softjin.com.

The company's headquarters are located at Unit No: 102, Mobius Tower, SJR I - Park, EPIP, White Field, Bangalore – 560066, Tel: +91-80- 41779999, E-mail: sales@softjin.com
The USA office is located at 2900 Gordon Ave, Suite 100-11, Santa Clara, CA 95051, Tel: (408) 773-1714, Email: sales_us@softjin.com

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Notes to editors:

Press Contact:

Georgia Marszalek, ValleyPR for SoftJin, +650-345 7477, Georgia@ValleyPR.com

A graphic is available on request.

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